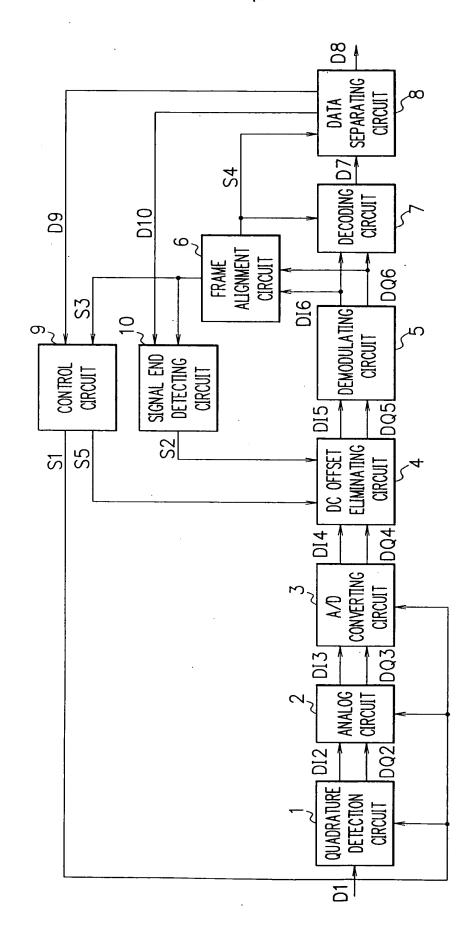
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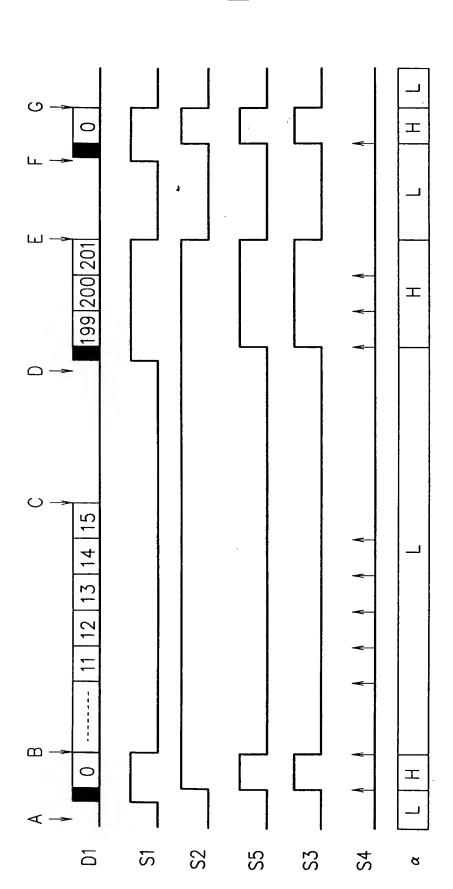




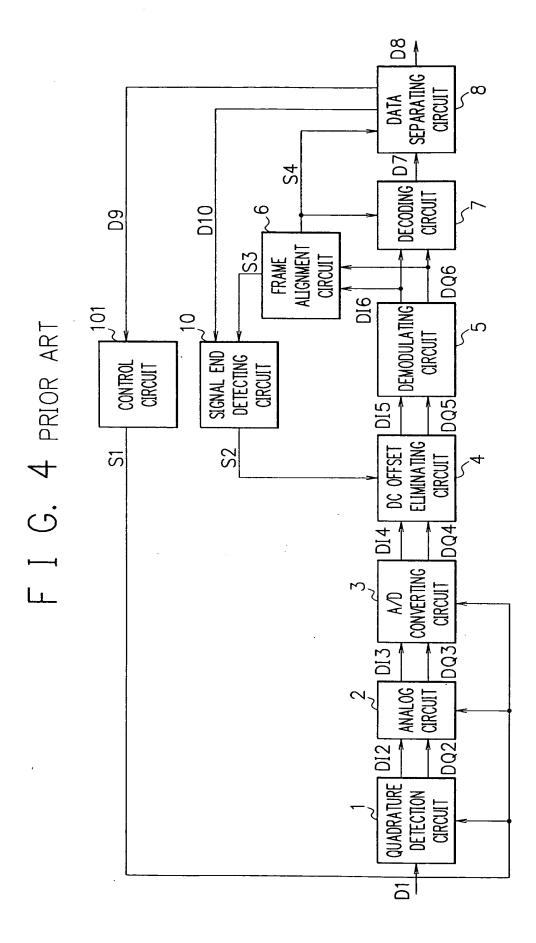
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005 56 27 DELAY CIRCU DELAY CIRCL 1-bit 24 25 ADDING CIRCUIT ADDING CIRCUIT MULTIPLYING MULTIPLYING 22 CIRCUIT CIRCUIT **S20** 78 SUBTRACTING SUBTRACTING DCQ DC1 CIRCUIT CIRCUIT AND CIRCUI DI4

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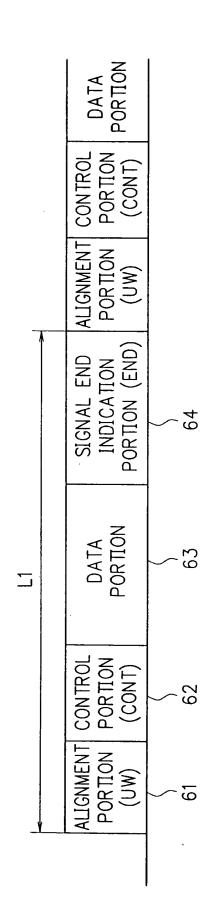
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005 DI5 **5**6 DELAY CIRCUI DELAY CIRCU 25 ADDING CIRCUIT ADDING CIRCUIT MULTIPLYING MULTIPLYING CIRCUIT CIRCUIT SUBTRACTING SUBTRACTING **5**0 CIRCUIT CIRCUIT **S**5 DI4

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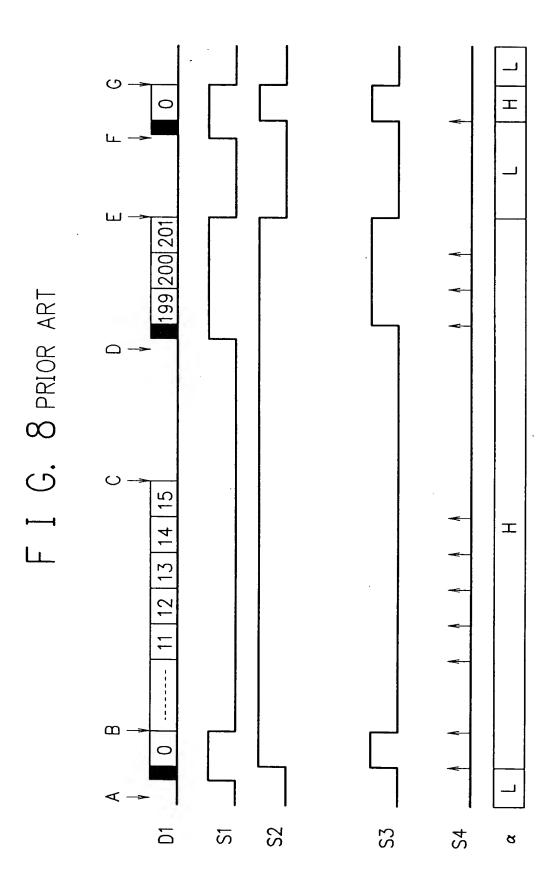


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DATA PORTION ALIGNMENT CONTROL PORTION (UW) SIGNAL END INDICATION PORTION (END) DATA PORTION PORTION (CONT) PREAMBLE ALIGNMENT PORTION $(M \cap M)$ PORTION (PA)

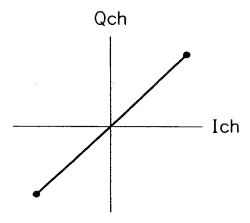
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